

<b>Notice of References Cited</b>	Application/Control No. 10/628,074		Applicant(s)/Patent Under Reexamination MOACANIN ET AL.	
	Examiner Cynthia Britt		Art Unit 2138	Page 1 of 1

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**NON-PATENT DOCUMENTS**

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	U	"Low-power synchronous-to-asynchronous- to-synchronous interlocked pipelined CMOS circuits operating at 3.3-4.5 GHz" by Schuster et al. This paper appears in: IEEE Journal of Solid-State Circuits Publication Date: Apr 2003 Volume: 38, Issue: 4 On page(s): 622- 630 ISSN: 0018-9200 INSPEC Accession Number: 7565233
	V	IBM TDB: NN8607813 "Method to Synchronize an Asynchronous IBM Series/1 Interface to Allow Testing on a Synchronous Tester" IBM Technical Disclosure Bulletin, July 1986, US VOLUME:29, ISSUE:2 PAGE: 813 - 815 Publication Date: July 1, 1986 (19860701)
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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